

# LESSON PLAN

Subject Code & subject name: 13EC2002 Electronic Circuits - I  
Class / Semester : II Year I Sem.

Branch : ECE  
Academic Year : 2016-17

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective action upon review
		<b>Unit-1</b>				
1.	21-06-17	Introduction of Filters	1	CR		
2.	29-06-17	Harmonic Components in rectifier circuits	1	CR		
3.	30-06-17	Inductor filter & capacitor filter	1	CR		
4.	30-06-17	L- Section filter II- section filter	1	CR		
5.	01-07-17	Multiple L-section filter Multiple II- section filter	1	CR		
6.	04-07-17	Comparison of filter circuits	1	CR		
7.	07-07-17	Simple regulators CKT	1	CR		
8.	07-07-17	Using zener diode	1	CR		
		<b>Unit-2</b>				
9.	11-07-17	Transistor biasing & stabilization	2	CR		
10.	13-07-17	Operating point & bias stability	2	CR		
11.	14-07-17	Collector to base bias self bias amplifiers	2	CR		
12.	14-07-17	Stabilization variations in V <sub>BE</sub> and B for the self bias circuit	2	CR		
13.	18-07-17	Stabilization factor $S$ , $S^1$ , $S^{11}$ ,	2	CR		
14.	20-07-17	Bias compensation Thermistor compensation	2	CR		
15.	03-08-17	Sinister compensation	2	CR		
16.	04-08-17	Compensation against variation against variation in $V_{BE}$ , $I_{CO}$ thermal runaway	2	CR		
17.	04-08-17	Thermal stability	2	CR		
18.	05-08-17	FET : As voltage variable resistor & Biasing	2	CR		
		<b>Unit-3</b>				
19.	11-08-17	Low frequency analysis of transistor	3	CR		
20.	11-08-17	Two port devices	3	CR		
21.	17-08-17	<b>Hybrid modal transistor hybrid model</b>	3	CR		
22.	18-08-17	Determination of h-parameters from characteristics	3	CR		
23.	18-08-17	Measurement of h-parameters	3	CR		
24.	29-08-17	Conversion formulas for the parameters of three transistor	3	CR		

		configurations .				
25.	01-09-17	Transistor amplifier circuits using h- parameters	3	CR		
26.	01-09-17	Comparison of transistor amplifier configurations	3	CR		
27.	07-09-17	FET: small signal	3	CR		
28.	08-09-17	Model & Analysis	3	CR		
		<b>Unit-4</b>				
29.	08-09-17	<b>Single stage Amplifiers</b>	4	CR		
30.	14-09-17	<b>Simplified common emitter hybrid model</b>	4	CR		
31.	15-09-17	Simplified calculation for common collector configuration	4	CR		
32.	15-09-17	Common base amp common emitter amp with emitter resistance	4	CR		
33.	16-09-17	Emitter follower	4	CR		
34.	17-09-17	Millers theorem & Dual of millers theorem	4	CR		
35.	29-09-17	FET: Common source	4	CR		
36.	29-09-17	Common drain Amplifiers	4	CR		
		<b>Unit-V</b>				
37.	30-10-17	High frequency Analysis	5	CR		
38.	05-10-17	Hybrid II –Common Emitter transistor model	5	CR		
39.	13-10-17	Hybrid II conductance	5	CR		
40.	15-10-17	<b>Hybrid II capacitance validity of hybrid –II model</b>	5	CR		
41.	17-10-17	<b>Variation of hybrid parameter CE short circuit</b>	5	CR		
42.	19-10-17	<b>Current gain with resistive load single stage CE transistor amplifier response.</b>	5	CR		
43.	20-10-17	<b>Gain bandwidth product</b>	5	CR		
44.	22-10-17	<b>Emitter follower at high frequency</b>	5	CR		
45.	22-10-17	<b>FET: common source</b>	5	CR		
46.	24-10-17	<b>Common drain amplifiers.</b>	5	CR		
47.	25-10-17	<b>I Unit University problems solved</b>	5	CR		
18.	27-10-17	<b>II Unit problems</b>	5	CR		
49.	27-10-17	<b>University questions solved</b>	5	CR		
50.	31-10-17	<b>III Unit IV Unit problems solved</b>	5	CR		
			5	CR		
			5	CR		

**Faculty Name : Prof. M. Jaya Manmadha Rao**

**CR: CLASS ROOM**

**OHP: OVERHEAD PROJECTOR**

**LCD**

**Text Books:**

1. Integrated Electronics – J. Millman and C.C. Halkias, Mc Graw Hill, 1972
2. Electronic Devices and Circuits – Salivahanan, N. Suresh Kumar, A. Vallavaraj, Tata Mc Graw Hill, 2/e

**Reference Books:**

1. Electronic Devices and Circuits Theory – Robert L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, 2006, 9/e
2. Micro Electronic Circuits – Sedra A.S. and K.C. Smith, Oxford University Press, 5/e

**FACULTY**

**FACULTY IN-CHARGE**

**HEAD OF THE DEPARTMENT**